

FIG. 1

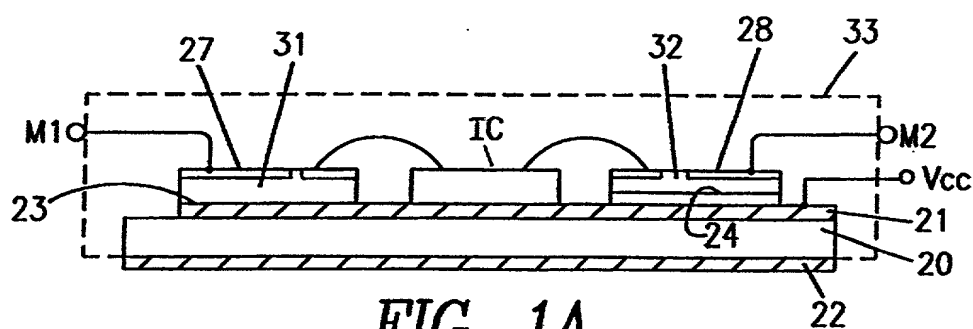


FIG. 1A

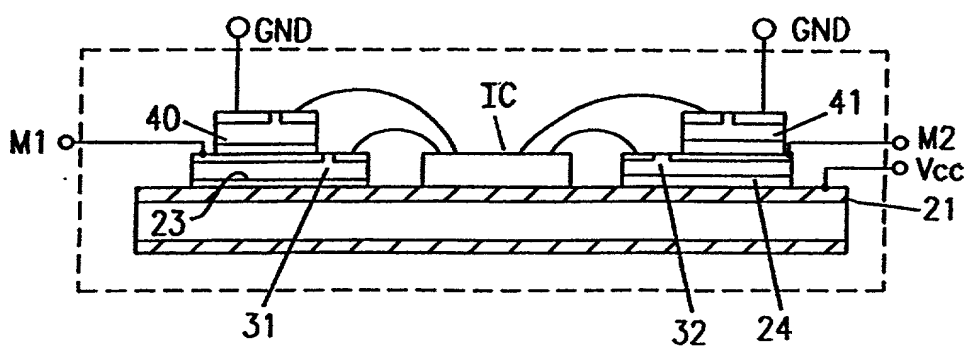


FIG. 1B

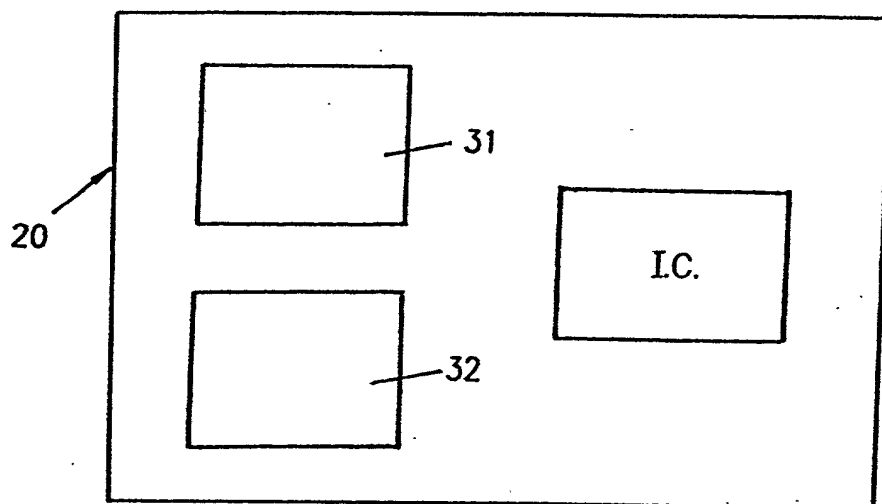


FIG. 1C

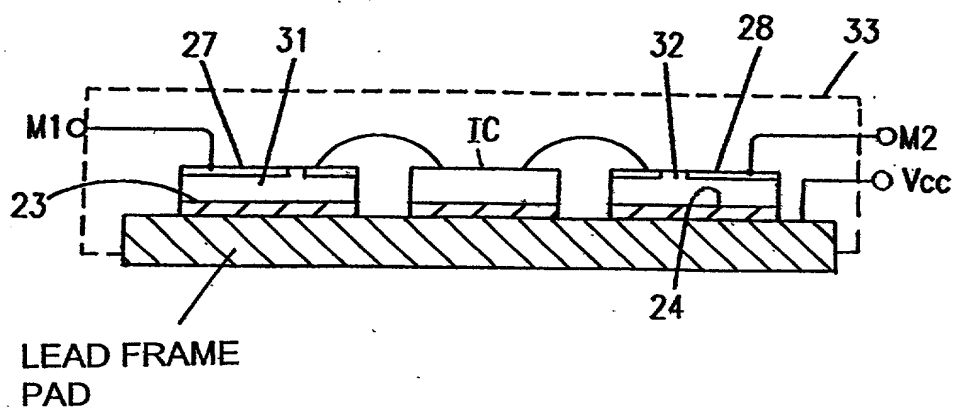


FIG. 1D



FIG. 2

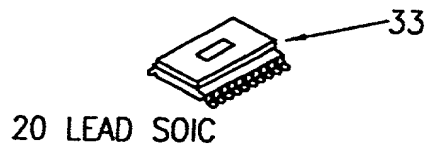


FIG. 4

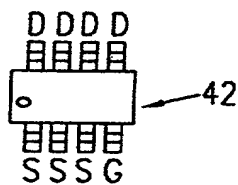


FIG. 3

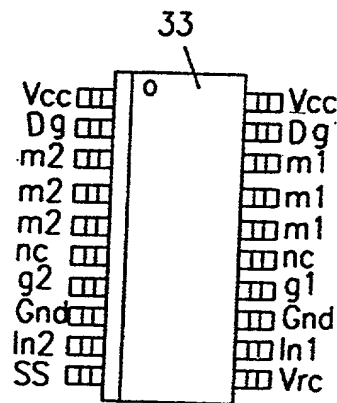


FIG. 5

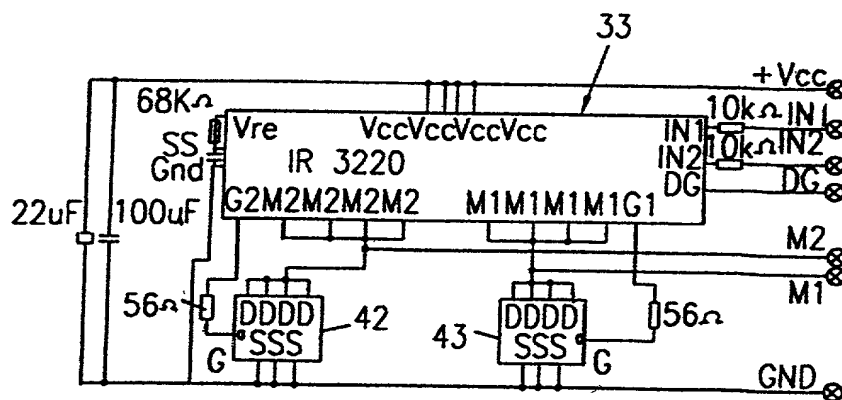


FIG. 6

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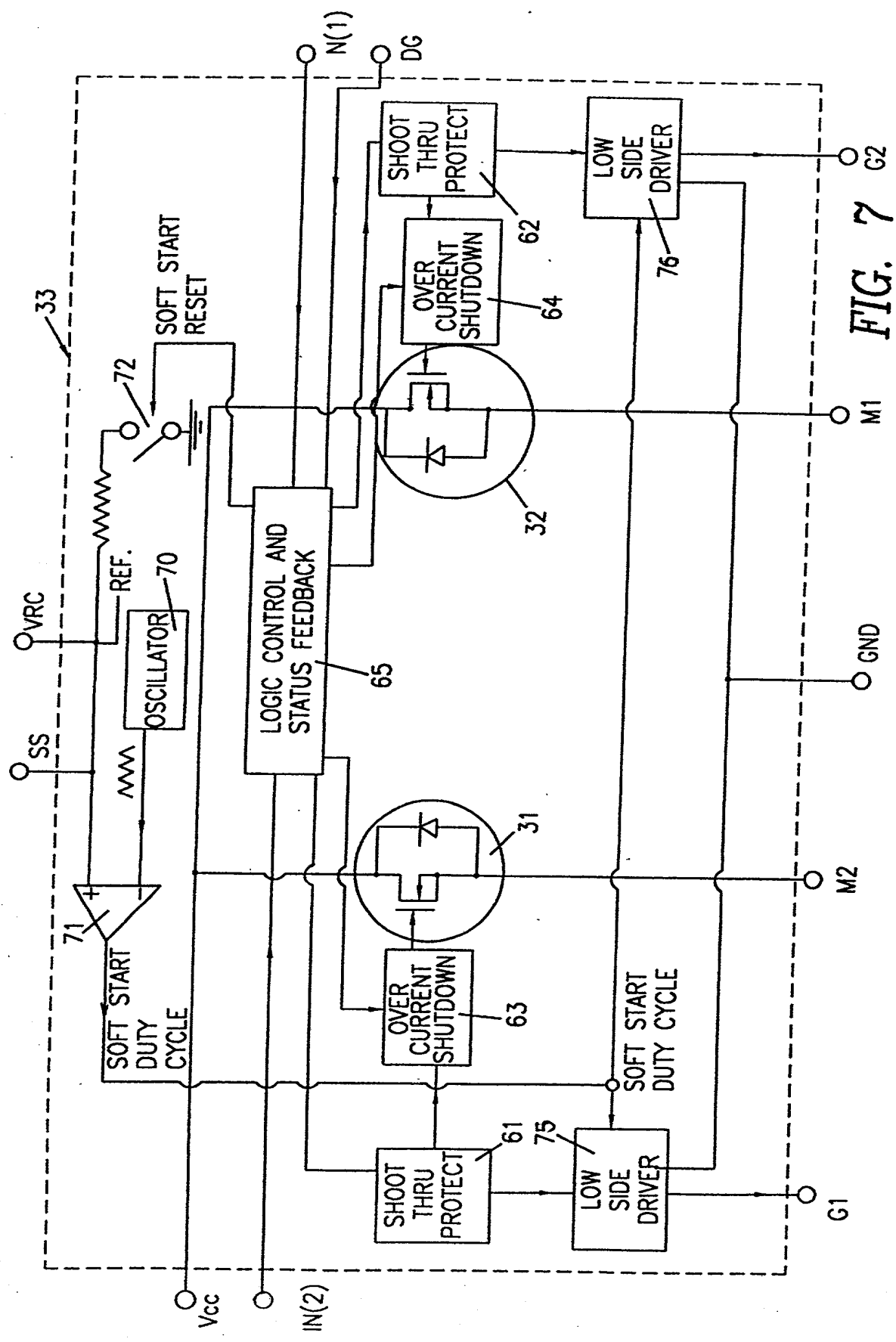


FIG. 7



[illegible]

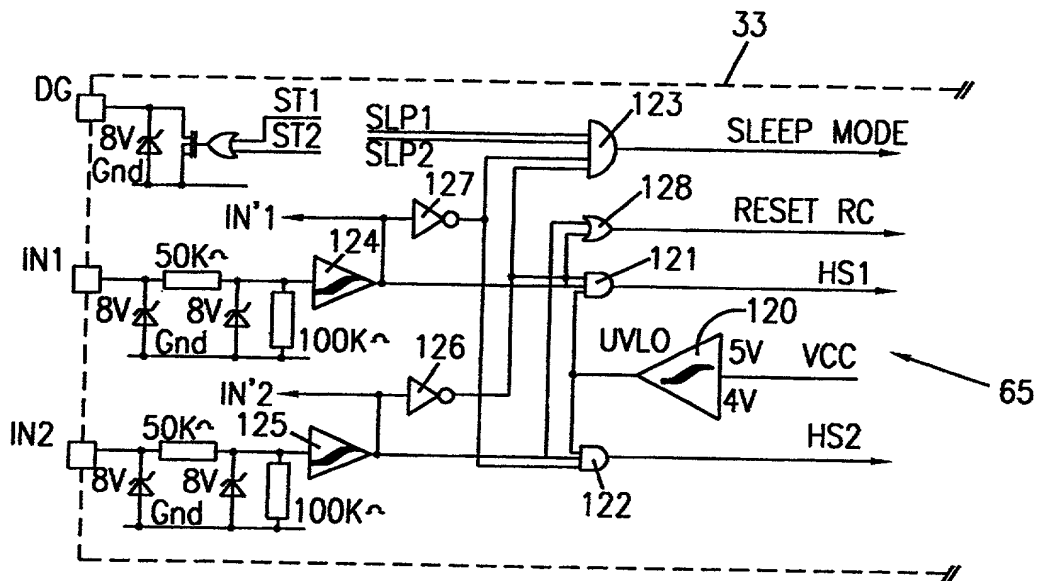
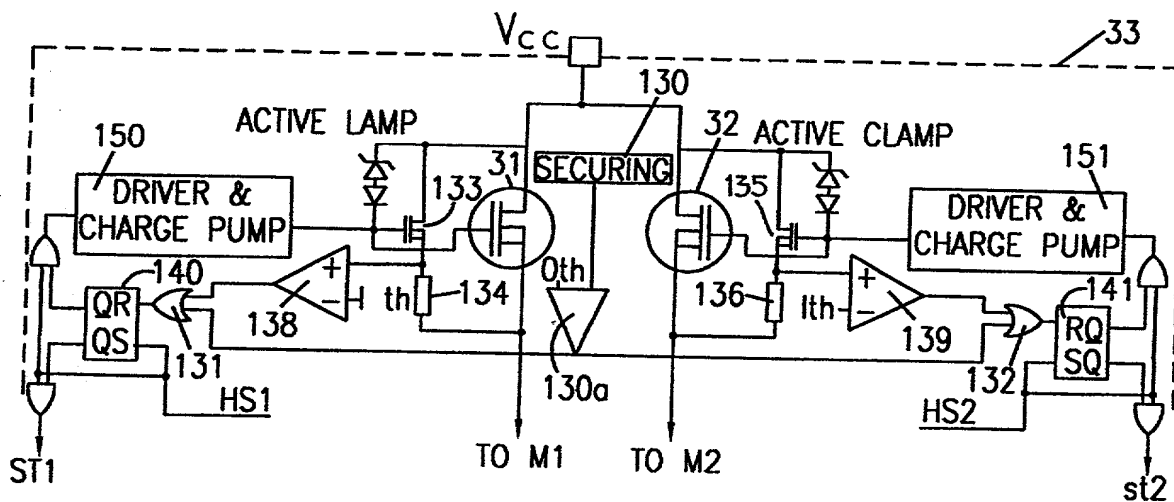


FIG. 11



SHUT DOWN CIRCUITS

FIG. 12

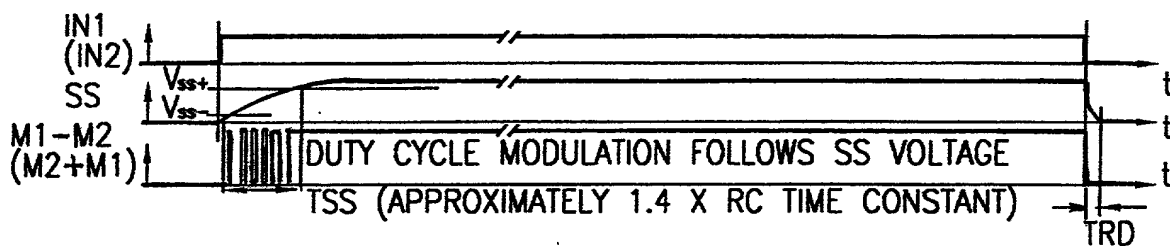


FIG. 13

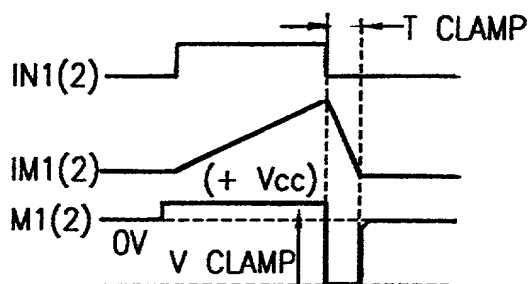


FIG. 14

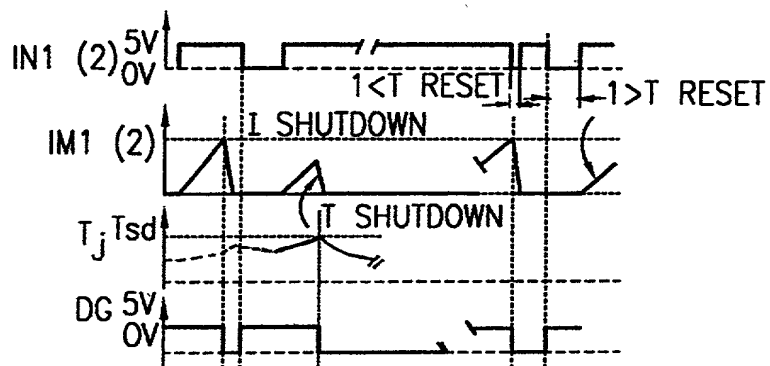


FIG. 15



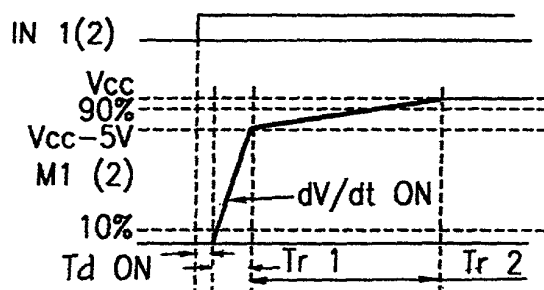


FIG. 16

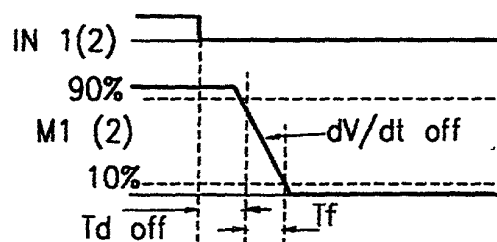


FIG. 17

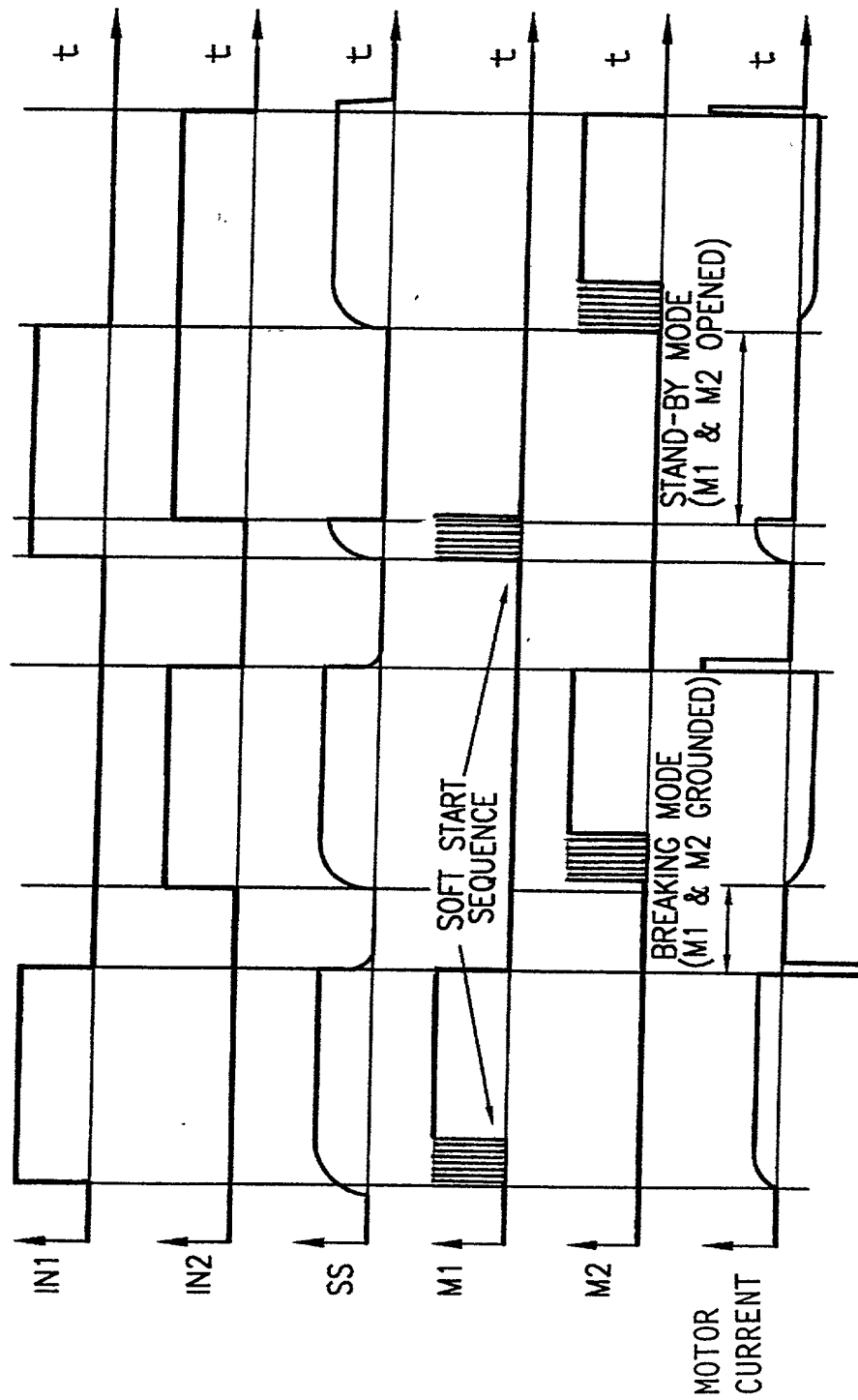


FIG. 18